

2818

PATENT
Docket No.: VST-001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2818

Examiner: Auduong, Gene N.

Serial No.: 10/002,607

Filed: October 30, 2001

In re Application of: Bergemont et al.

For: COMMON SOURCE EEPROM AND FLASH MEMORY

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Director of Patents, Box Non-Fee Amendment, Washington, D.C. 20231-0001, on 2-03-03, Signed T. Bounds
Tina Bounds

TRANSMITTAL LETTER

Director of Patents
Box Non-Fee Amendment
Washington, D.C. 20231

Sir:

Enclosed herewith for filing with respect to the above-identified patent application, please find:


1. Amendment and Response to Office Action dated 11/06/02.

In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, the Assistant Commissioner is hereby authorized to charge or credit the difference to our Deposit Account No. 50-0612.

Respectfully submitted,
Sierra Patent Group, Ltd.

Dated: February 3, 2003

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William P. Wilbar
Reg. No. 43,265

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AMENDMENT AND RESPONSE TO OFFICE ACTION

Director of Patents
Box Non-Fee Amendment
Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated November 6, 2002, kindly amend the above-identified application as follows.

In the Claims

Please cancel claims 2, 3, 5, 6, 8, and 9.

Please amend claims 1, 4, and 7 as follows.

1. An array of nonvolatile memory cells arranged in a plurality of rows and a plurality of columns comprising:

a substrate upon which said array is deposited;

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